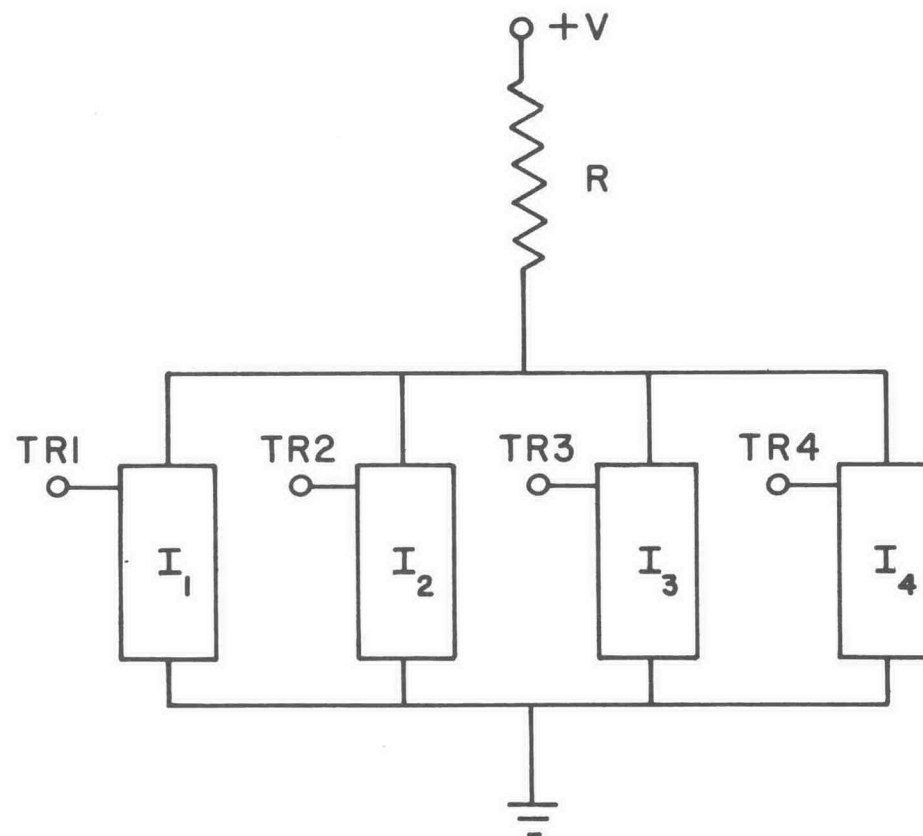




TR1, TR2, TR3, TR4 – TRIGGER INPUTS

Fig. 15. Operational schematic of velocity and tilt circuits.